Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L2	0	@ad<"20011204" and (i2c with rs232 with fifo with super)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/07/28 08:49
L3	0	@ad<"20011204" and (i2c with rs232 with fifo)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/07/28 08:50
L4	15	@ad<"20011204" and (i2c with rs232)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/07/28 08:50
L6	0	(UART with FIFO) and @ad<"20011204" and super and i2c	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/07/28 08:55
L7	1	(UART with FIFO) and @ad<"20011204" and super and (internal adj5 integrat$3)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/07/28 09:57
L8	0	(UART with FIFO) and @ad<"20011204" and super and (internal adj integrat$3)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/07/28 08:59
L9	7	(UART with FIFO) and @ad<"20011204" and super and (integrat$3 adj circuit$3)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/07/28 09:01
L10	0	(UART with FIFO) and @ad<"20011204" and super and (inter adj integrat$3)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/07/28 09:01
L11	3	(UART with FIFO) and @ad<"20011204" and (inter adj integrat$3)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/07/28 09:21
L12	0	@ad<"20011204" and (inter adj integrat$3) and (super adj5 control$3) and rs232	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/07/28 09:39
L13	8	@ad<"20011204" and (inter adj integrat$3) and (super adj5 control$3)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/07/28 09:23
L14	0	@ad<"20011204" and (inter adj integrat$3) and (super adj5 control$3) and duart	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/07/28 09:23
L15	1	@ad<"20011204" and (inter adj integrat$3) and (super adj5 control$3) and uart	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/07/28 09:24
L16	8	@ad<"20011204" and (inter adj integrat$3) and (super adj5 control$3) and isa	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/07/28 09:40
L17	0	(UART with FIFO) and @ad<"20011204" and super and (inter adj5 integrat$3)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/07/28 09:57

L19	1	"6145085".pn. and PCI and ISA and Super	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/07/28 14:33
S1	2	("6356130" or "6574751")	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/01/25 09:11
S2	1	"20030105875"	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/01/25 09:10
S3	2	"709"/$.ccls. and (RS232 or (RS adj "232")) and UART and SOC	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/01/25 13:23
S4	33	(RS232 or (RS adj "232")) and UART and SOC and @ad<"20011204"	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/01/25 09:33
S5	8	(RS232 or (RS adj "232")) and UART and SOC and @ad<"20011204" and server and bus and FIFO	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/01/25 09:35
S6	0	RS232 and (UART with FIFO) and SOC and @ad<"20011204" and server and bus	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/01/25 16:45
S7	120	"709"/$.ccls. and (RS232 or (RS adj "232")) and UART and transmission and control and "I/O"	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/01/25 13:25
S8	30	"709"/$.ccls. and (RS232 or (RS adj "232")) and UART and transmission and control and "I/O" and PCI	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/01/26 15:48
S9	7	S8 and SDRAM	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/01/25 13:27
S10	8	RS232 and UART and SOC and @ad<"20011204" and server and bus and FIFO	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/01/25 13:53
S11	25	(RS232 or (RS adj "232")) and UART and (transmission or trans$6) and control and "I/O" and PCI and SDRAM and chip	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/01/25 13:59
S12	10	(RS232 or (RS adj "232")) and UART and (transmission or trans$6) and control and "I/O" and PCI and SDRAM and chip and @ad<"20011204"	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/01/25 14:03
S13	4	(RS232 or (RS adj "232")) and UART and (transmission or trans$6) and control and "I/O" and PCI and server and SDRAM and chip and @ad<"20011204"	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/01/25 15:07

S14	0	(RS232 or (RS adj "232")) and PCI and server and SDRAM and I2C and @ad<"20011204"	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/01/25 15:08
S15	7	(RS232 or (RS adj "232")) and PCI and server and I2C and @ad<"20011204"	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/01/25 15:09
S16	7	(RS232 or (RS adj "232")) and PCI and server and I2C and trans$6 and @ad<"20011204"	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/01/25 15:17
S17	16	(RS232 or (RS adj "232")) and PCI and (super near "i/o") and @ad<"20011204"	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/01/25 15:31
S18	1	(RS232 or (RS adj "232")) and PCI and (super near "i/o") and (I2C or (inter with integrat$3)) and @ad<"20011204"	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/01/25 15:32
S19	33	RS232 and (UART with FIFO) and @ad<"20011204"	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/01/26 15:47
S20	0	RS232 and (UART with FIFO) and @ad<"20011204" and (super near "i/o")	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/01/25 16:48
S21	0	RS232 and (UART with FIFO) and @ad<"20011204" and super	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/01/25 16:48
S22	8	(UART with FIFO) and @ad<"20011204" and super	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/07/28 08:57
S23	1	"6845410"	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/01/26 15:47
S24	30	"709"/$.ccls. and (RS232 or (RS adj "232")) and UART and transmission and control and "I/O" and PCI	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/01/26 15:48
S25	30	"709"/$.ccls. and (RS232 or (RS adj "232")) and UART and transmission and control and "I/O" and PCI	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/01/26 15:49
S28	9	(i2c adj5 bus adj5 switch) and @ad<"20011204"	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/07/27 17:36
S30	1	(i2c adj5 bus adj5 switch) and @ad<"20011204" and (i2c with pci)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/07/28 08:48